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2827
#9
election
JWH
6-26-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Takashi Kumamoto et al.
Serial No. : 09/878,123
Filed : June 8, 2001
Title : CHIP LEAD FRAMES

Art Unit : 2827
Examiner : Luan C. Thai

Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed April 9, 2002, applicant elects the invention of Group I drawn to a semiconductor device. The election is made without traverse.

Please apply any other charges to Deposit Account No. 06-1050.

Respectfully submitted,

Date: _____

5/9/02

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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